

7-12-023

843.41231X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): KAWAKAMI et al.
Serial No.: Not yet assigned
Filed: On even date
For: METHOD OF MANUFACTURING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE AND
SEMICONDUCTOR MANUFACTURING APPARATUS
Group: Not assigned

JC979 U.S. PTO
10/084943
03/01/02

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 & 1.98

Assistant Commissioner
for Patents
Washington, D.C. 20231

March 1, 2002

Sir:

In the matter of the above-identified application, applicants are submitting herewith copies of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted with the filing of the new application.

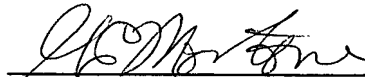
To the extent the documents listed on the attached form equivalent to Form PTO-1449 are not in the English language, the requirement of 37 CFR 1.98(a)(3) for a concise explanation of the relevance is satisfied by English language abstracts of the documents provided herewith and the discussion of these documents in the specification, for example, beginning on page 3.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (843.41231X00) please credit any excess fees to such deposit account.

Respectfully submitted,

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